FIG. 1A

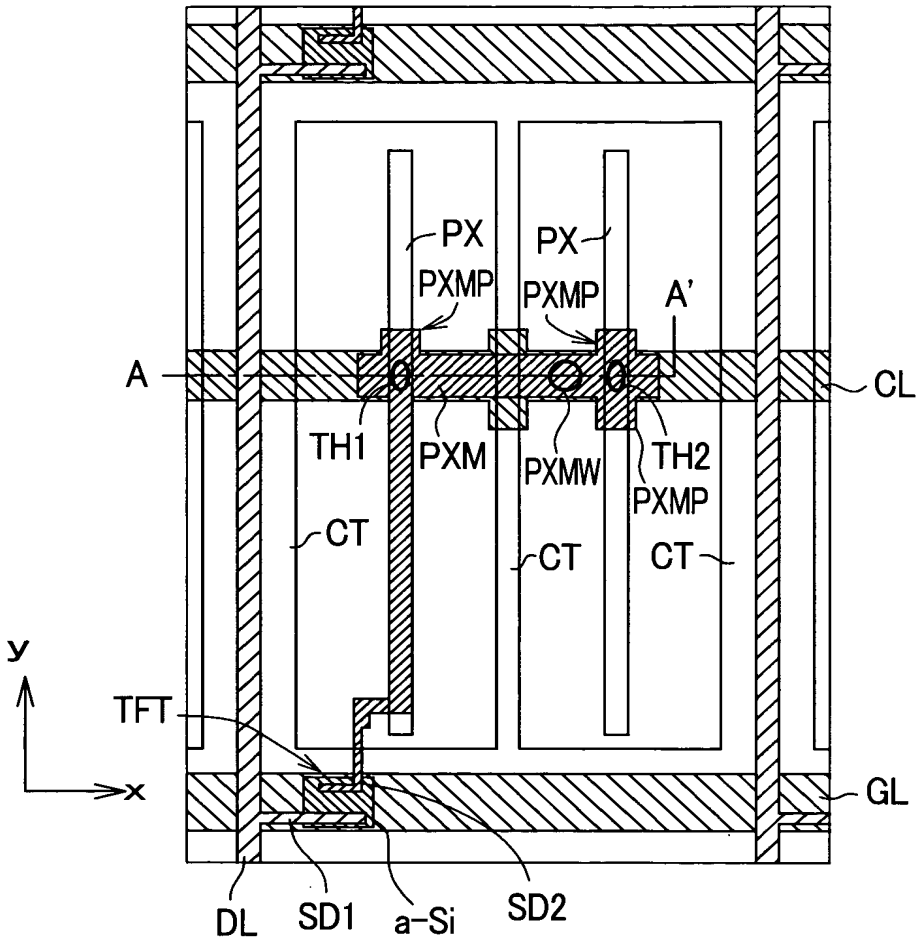
PX
PXMP
PX
PXMP
A'
A
CL
TH1
PXM
PXMW
TH2
PXMP
CT
CT
CT
y
TFT
x
GL
DL
SD1
a-Si
SD2

FIG. 1B

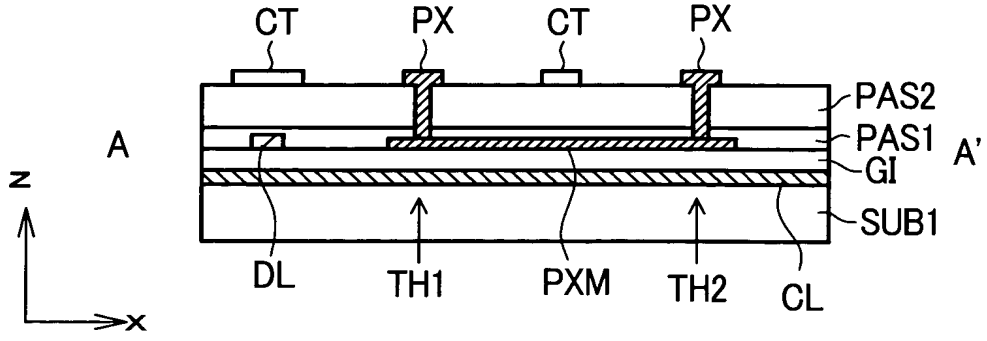
CT
PX
CT
PX
PAS2
A
PAS1
A'
GI
z
SUB1
DL
TH1
PXM
TH2
CL
x

FIG. 2

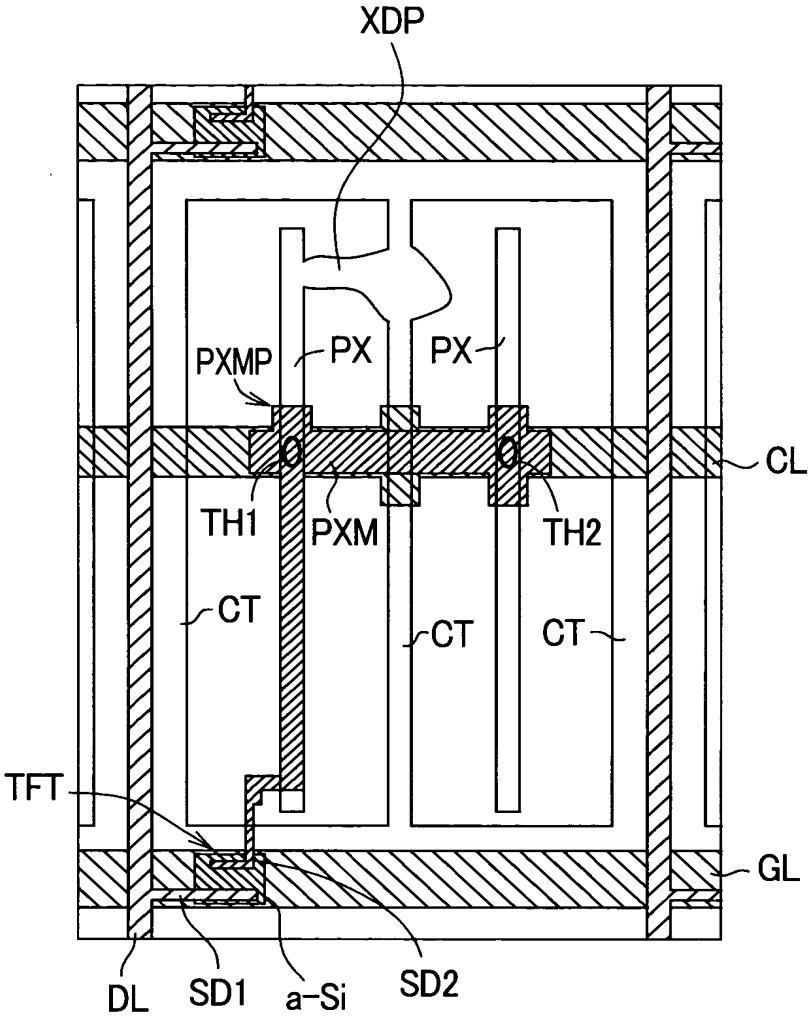

FIG. 3

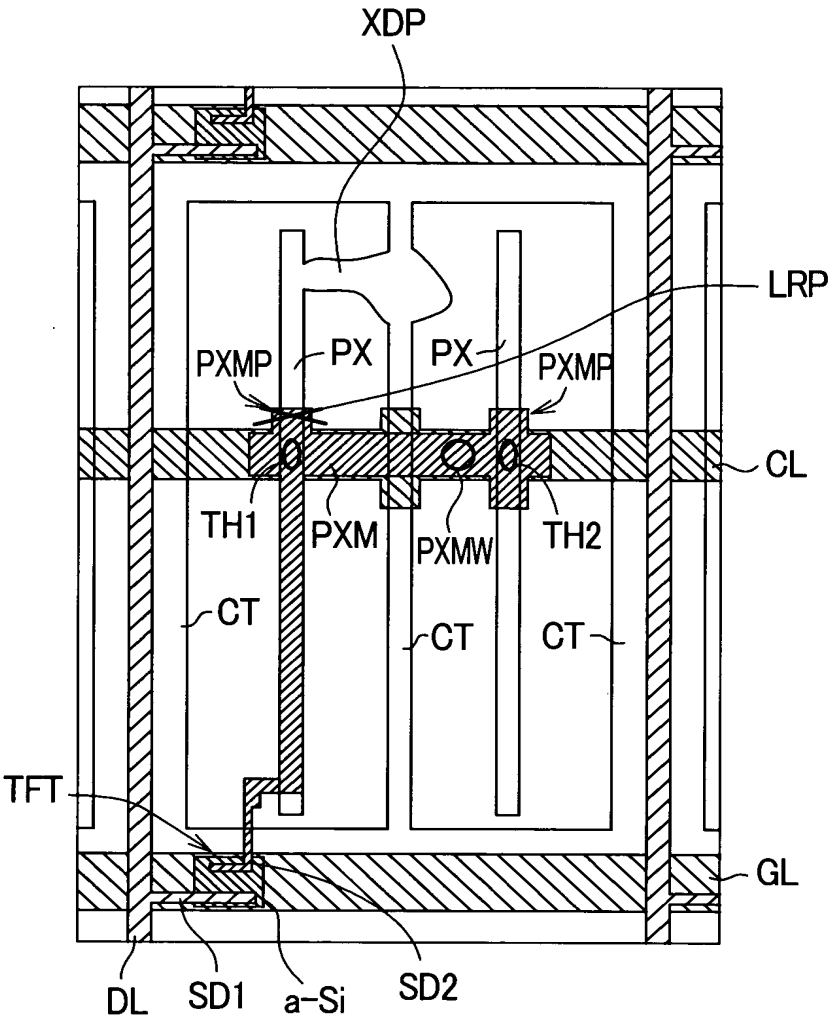
XDP
LRP
PXMP
PX
PX
PXMP
CL
TH1
PXM
PXMW
TH2
CT
CT
CT
TFT
GL
DL
SD1
a-Si
SD2

FIG. 4

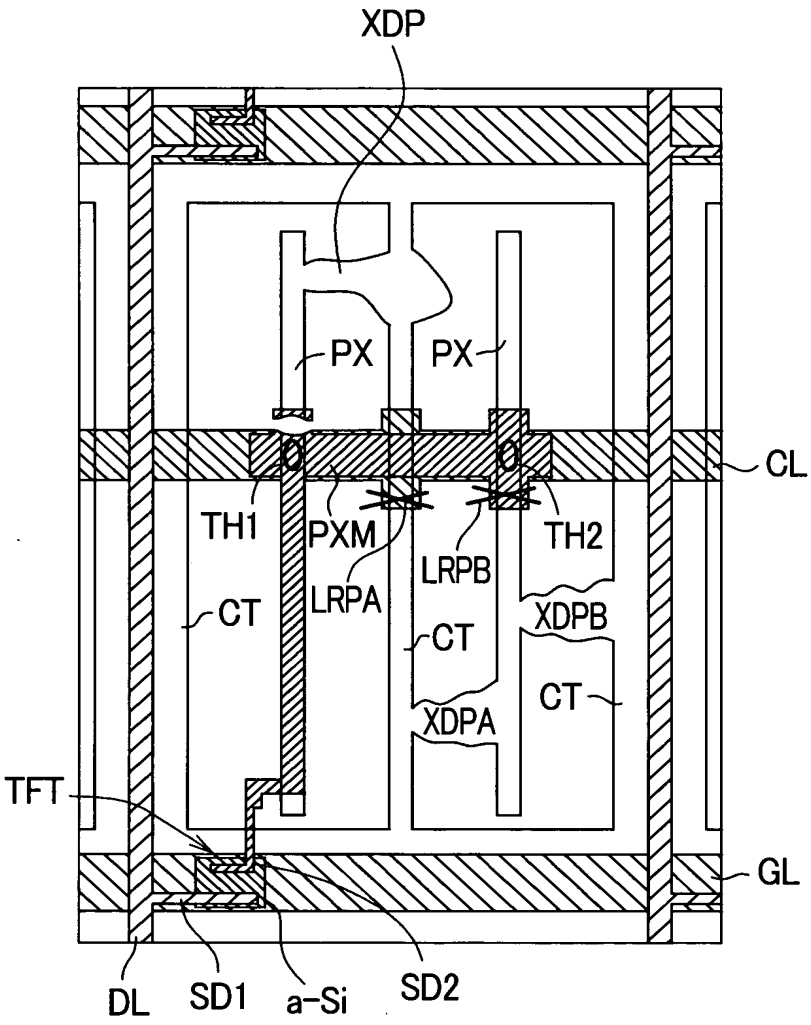
XDP
PX
PX
CL
TH1
PXM
LRPB
TH2
LRPA
CT
CT
XDPB
CT
XDPA
TFT
GL
DL
SD1
a-Si
SD2

FIG. 5

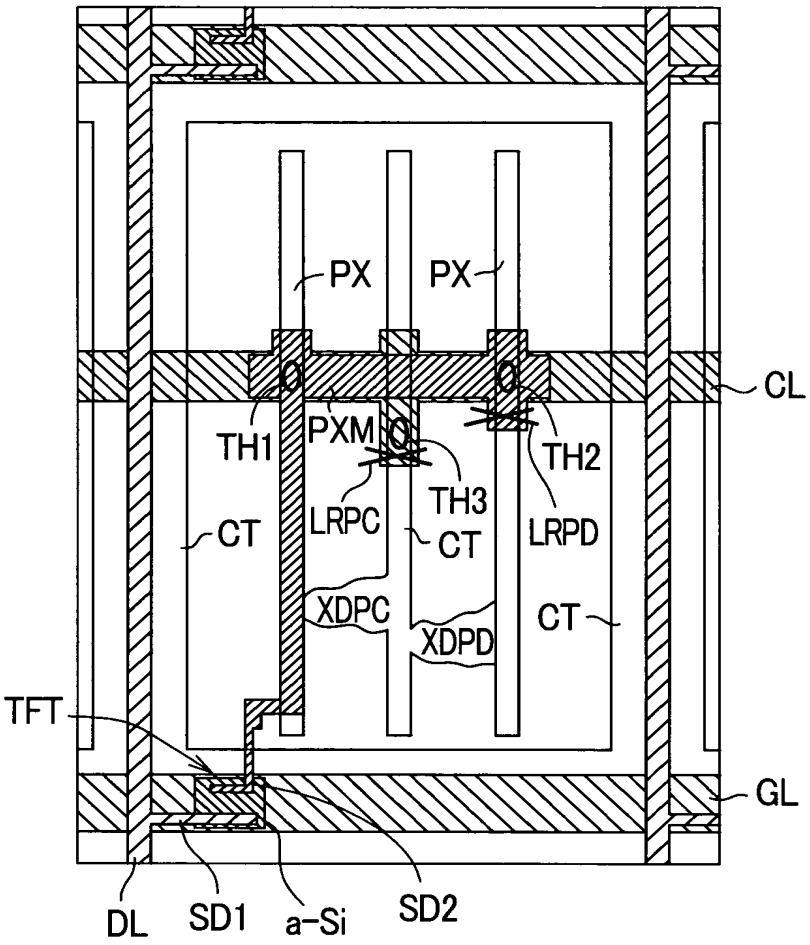
PX
PX
CL
TH1
PXM
TH2
TH3
LRPC
CT
CT
LRPD
XDPC
CT
XDPD
TFT
GL
DL
SD1
a-Si
SD2

FIG. 6

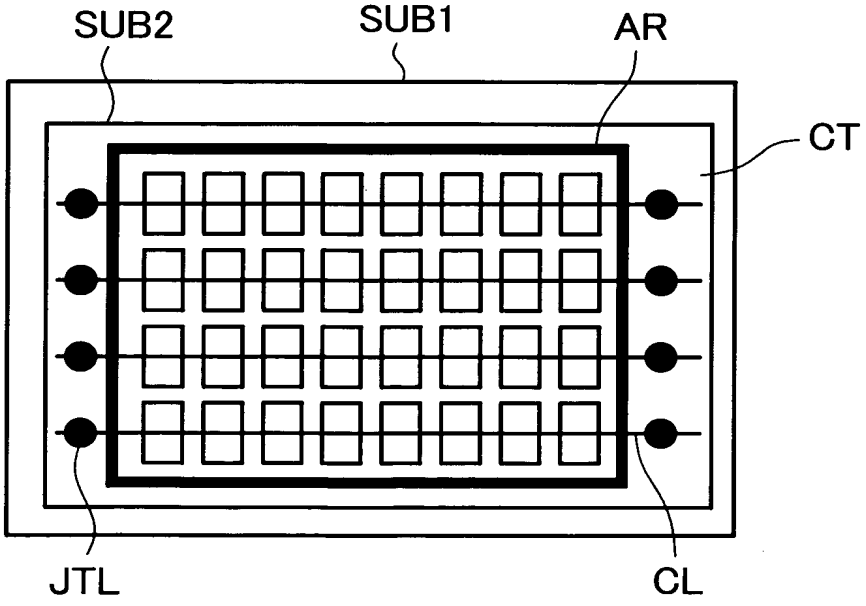
SUB2
SUB1
AR
CT
JTL
CL

FIG. 7A

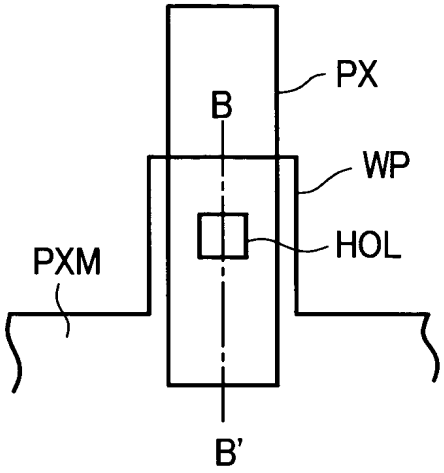

FIG. 7B

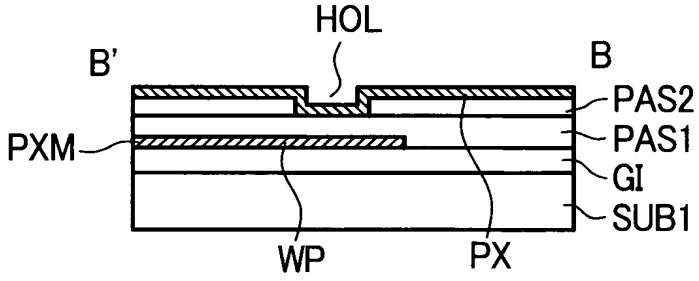

FIG. 8A

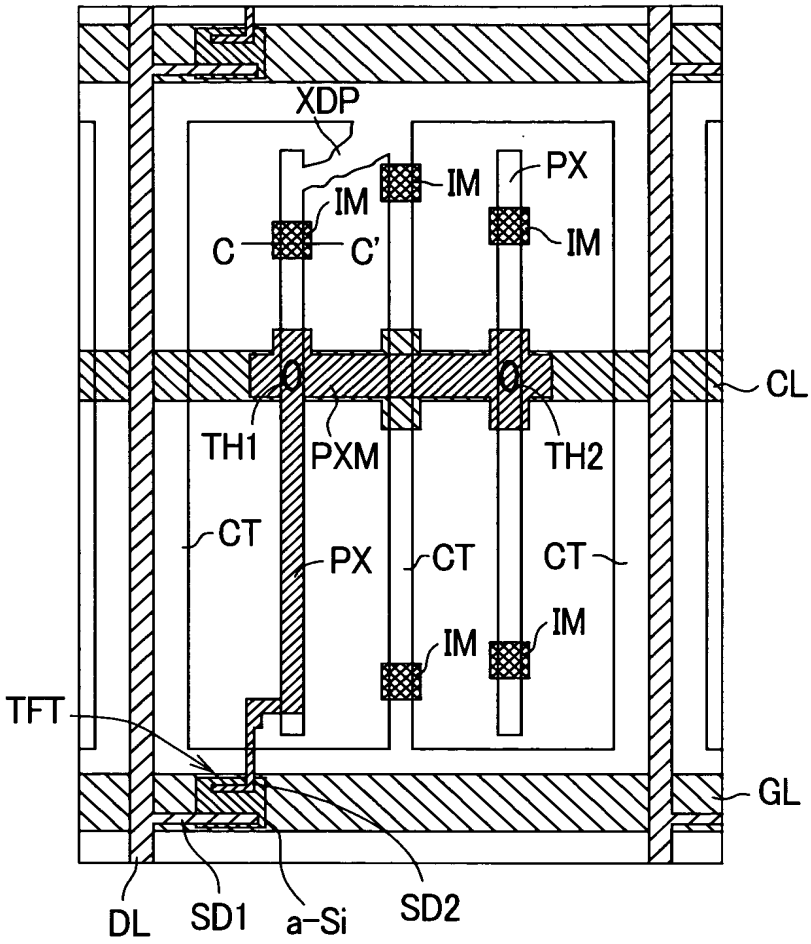
XDP
IM
IM
PX
IM
C
C'
IM
CL
TH1
PXM
TH2
CT
PX
CT
CT
IM
IM
TFT
GL
DL
SD1
a-Si
SD2

FIG. 8B

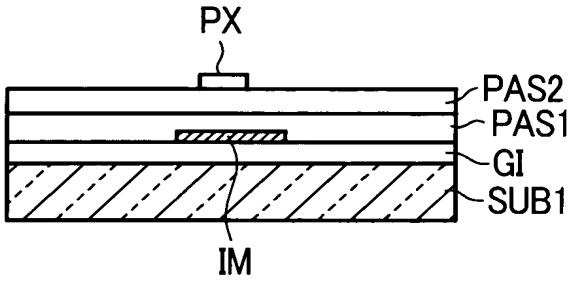
PX
PAS2
PAS1
GI
SUB1
IM

FIG. 9

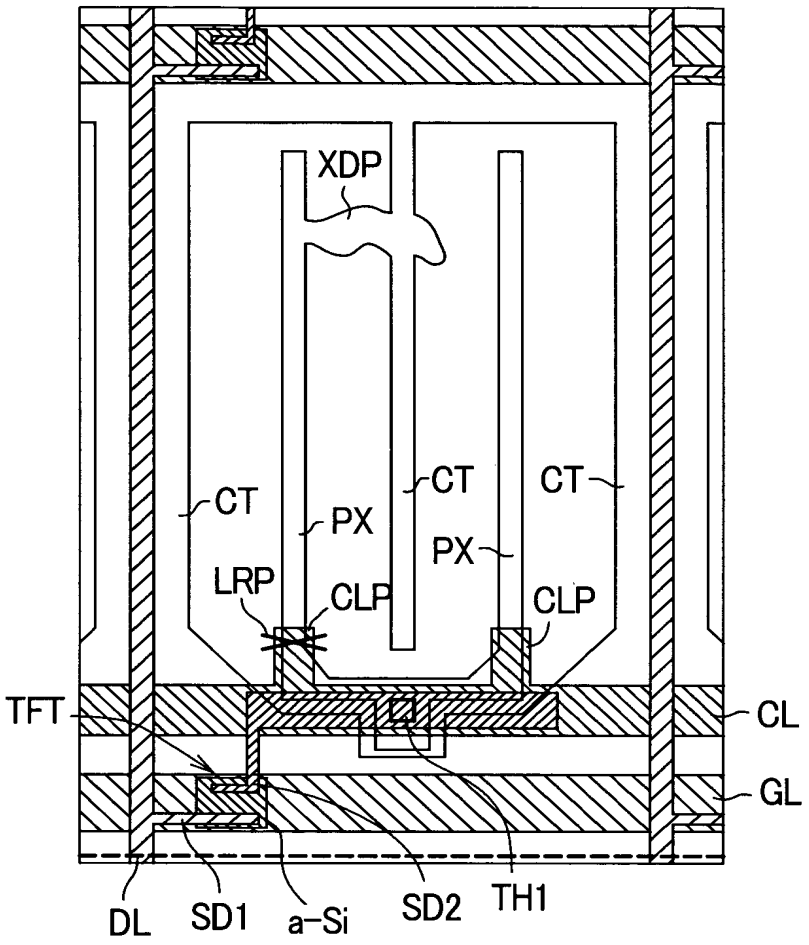

FIG. 10

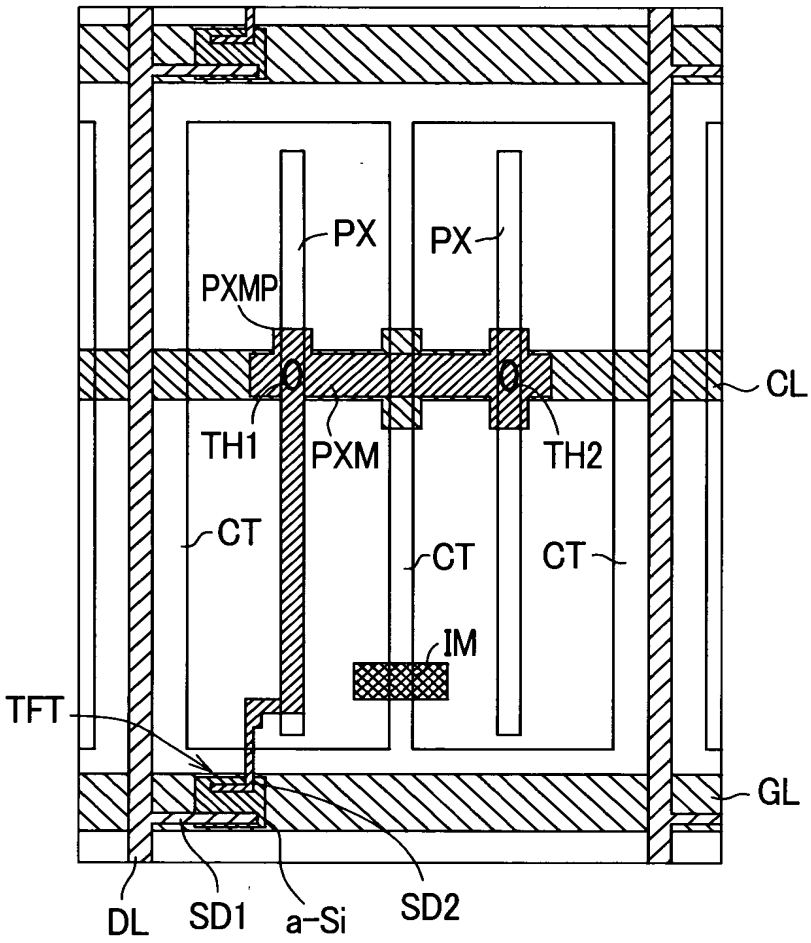

FIG. 11A

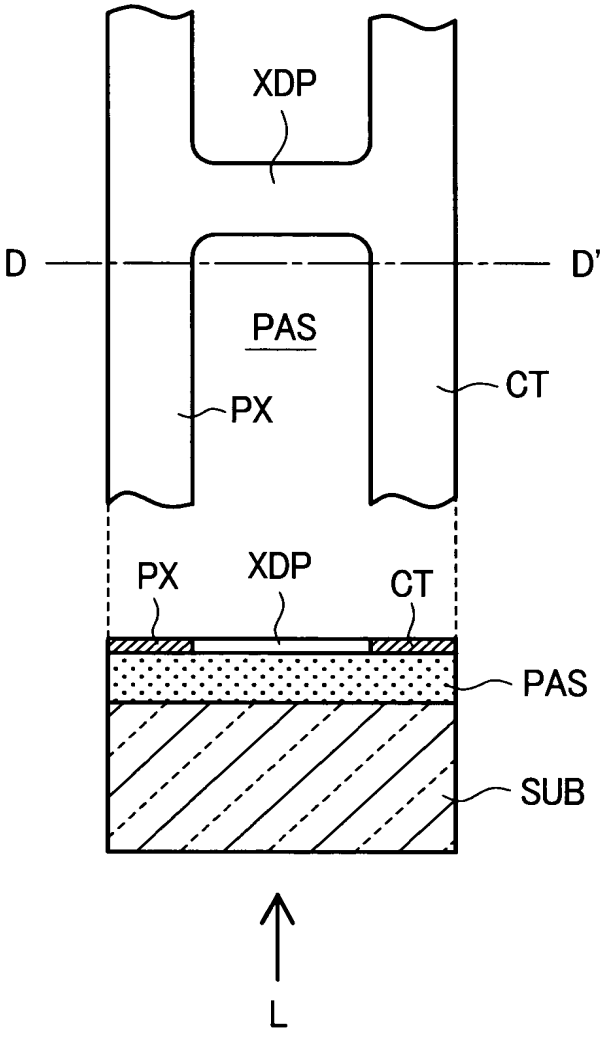

FIG. 11B

FIG. 12A

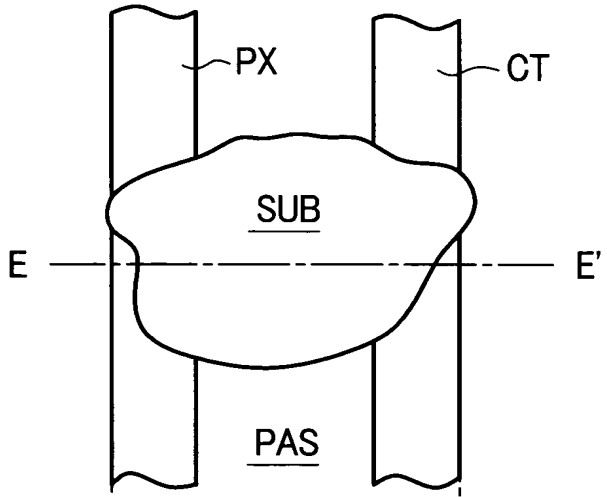

FIG. 12B